



EV077330119

Form PTO-151		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. M122-1921		SERIAL NO. 10/071,453	
LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)				APPLICANT Luan C. Tran			
				FILING DATE February 8, 2002		GROUP 2813	
U.S. PATENT DOCUMENTS							
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
<i>HL</i>	AA	5,447,884	09/05/95	Fahey et al.			
<i>HL</i>	AB	5,851,900	12/22/98	Chu et al.			
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						
	AK						
FOREIGN PATENT DOCUMENTS							
		Document Number	Date	Country	Class	Subclass	Translation
							Yes No
	AL						
	AM						
	AN						
	AO						
	AP						
OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
<i>HL</i>	AQ		Wolf et al., "Silicon Processing for the VLSI Era" - Volume 1 - Process Technology - Second Edition. © 2000, 6 pages.				
	AR						
	AS						
EXAMINER <i>Wesley Salty</i>				DATE CONSIDERED <i>10/23/02</i>			
*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

RECEIVED
OCT 10 2002
TECHNOLOGY CENTER 2800